Personal Profile

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Educational Qualification:

M.Sc.: M.Sc. in Physics with specialization in Electronics from Guru Ghasidas University (Central University) in the year of 2012.

Ph.D.: Physics (Pursuing), Bankura University, Bankura (2019-20 onwards)

Research Topic: "Some studies on alternative method of frequency encoded Optical logic gates and processors"

Area of Teaching: General properties of matter, Electricity, Electrostatics, Thermodynamics, Statistical Mechanics, Atomic physics, Quantum Mechanics, etc.

Research Interest: Optical communication, Opto-electronic devices, Optical amplifier

International Publications:

Sl. No.	Title, Authors, Journal, Book, Publishing year	ISSN / ISBN Number
1.	Baibaswata Bhattacharjee, and **Surajit Bosu**, "A novel approach of developing all-optical frequency encoded dibit-based Peres gate using reflective semiconductor optical amplifier", **Journal of Nonlinear Optical Physics & Materials** (2022). **World Scientific**. **Indexed in: SCI (Expanded), UGC CARE List, Impact Factor 0.989** https://doi.org/10.1142/s0218863523500224	ISSN (print): 0218-8635 | ISSN (online): 1793-6624
2.	**Surajit Bosu and Baibaswata Bhattacharjee**, "A design of all-optical read-only memory using reflective semiconductor optical amplifier," **Journal of Optics,** (2022)**, pp**. 1-11**, Springer** **Indexed in: Scopus, ESCI, UGC CARE List** https://doi.org/10.1007/s12596-022-00943-8	Electronic ISSN-0974-6900 Print ISSN-0972-8821
3.	**Surajit Bosu** and Baibaswata Bhattacharjee, "All-optical dibit-based Feynman gate using reflective semiconductor optical amplifier with frequency encoding scheme**," Journal of Optics**, (2022), pp. 1-9. **Springer** **Indexed in: Scopus, ESCI, UGC CARE List** https://doi.org/10.1007/s12596-022-00875-3	Electronic ISSN-0974-6900 Print ISSN-0972-8821
4.	**Surajit Bosu**, and Baibaswata Bhattacharjee, "All-optical frequency encoded dibit-based parity generator using reflective semiconductor optical amplifier with simulative verification," **Facta Universitatis. Series: Electronics and Energetics ,** Vol. 35, No. 1 (2022), pp 029-041. **Indexed in: ESCI, UGC CARE List** https://doi.org/10.2298/fuee2201029b	Print ISSN: 0353-3670 Online ISSN: 2217-5997
5.	**Surajit Bosu,** and Baibaswata Bhattacharjee, "All-Optical Frequency Encoded 2-bit Comparator using Dibit-based logic and Reflective Semiconductor Optical Amplifier", **International Journal of Nanoparticles, (2022). (Inderscience)** **Indexed in: Scopus, UGC CARE List** https://doi.org/ 10.1504/IJNP.2022.10046913	ISSN online 1753-2515 ISSN print 1753-2507
6.	**Surajit Bosu** and Baibaswata Bhattacharjee, "A novel design of frequency encoded multiplexer and demultiplexer systems using reflected semiconductor optical amplifier with simulative verification," **Journal of Optics, vol.4, No. 3 (2021), pp. 361-370.** **Indexed in: Scopus, ESCI, UGC CARE List** **https://doi.org/10.1007/s12596-021-00711-0**	Electronic ISSN-0974-6900 Print ISSN-0972-8821

International Conferences:

Sl. No.	Name of Seminar / Conferences	Duration /Venue	International	Nature of Participation
1.	3rd International Conference on Communication and Intelligent Systems (**ICCIS 2021**)	18-19th December, 2021, NIT Delhi, India,	International	Paper Presentation
2.	5th International Conference in Communication, Devices, and Networking (**ICCDN 2021**)	15-16th December, 2021, Sikkim Manipal Institute of Technology (SMIT), India,	International	Paper Presentation
3.	5th International Conference in Communication, Devices, and Networking (**ICCDN 2021**)	15-16th December, 2021, Sikkim Manipal Institute of Technology (SMIT), India,	International	Paper Presentation
4.	International Conference on Paradigms of Communication, Computing and Data Sciences (**PCCDS-2021**)	7-9th MAY, 2021, NIT Kurukshetra	International	Paper Presentation
5.	Devices for Integrated Circuit (**DevIC-2021**)	19-20th May, 2021, Kalyani Govt. Engineering College, West Bengal, India	International	Paper Presentation
6.	Devices for Integrated Circuit (**DevIC-2021**)	19-20th May, 2021, Kalyani Govt. Engineering College, West Bengal, India	International	Paper Presentation
7.	International Virtual Conference on applied Science, Technology, Management and Language Studies (**ASTMLS -2020)**	11-12 December,2020, SONA COLLEGE OF TECHNOLOGY, Tamilnadu, India.	International	Paper Presentation

8.	International Virtual Conference on applied Science, Technology, Management and Language Studies **(ASTMLS -2020)**	11-12 December,2020, SONA COLLEGE OF TECHNOLOGY, Tamilnadu, India.	International	Paper Presentation

Book chapters:

Sl. No.	Title of the Book	Chapter Name	Authors	Publisher/ Date of publication	ISBN No.
1.	Algorithms for Intelligent Systems (**AIS**), Proceedings of the International Conference on Paradigms of Communication, Computing and Data Sciences	All-Optical Frequency Encoded Dibit-Based Half Subtractor Using Reflective Semiconductor Optical Amplifier with Simulative Verification	**Surajit Bosu**, Baibaswata Bhatterjee,	Springer Singapore, 2022	**ISBN:** 978-981-16-5746-7, https://doi.org/10.1007/978-981-16-5747-4_3
2.	Lecture Notes in Electrical Engineering (**LNEE**, Vol. 902), Advances in Communication, Devices and Networking (ICCDN 2021)	Dibit-Based 4-bit Parity Generator using Reflective Semiconductor Optical Amplifier and Frequency Encoding Scheme	**Surajit Bosu**, Baibaswata Bhatterjee	Springer Singapore, 2022	**ISBN:** 978-981-19-2003-5 https://doi.org/10.1007/978-981-19-2004-2_5
3.	Lecture Notes in Electrical Engineering (**LNEE**, Vol. 902), Advances in Communication, Devices and Networking (ICCDN 2021)	All-Optical Feynman Gate Using Frequency Encoding Scheme, Add/Drop Multiplexer and Reflective Semiconductor Optical Amplifier with Simulative Verification	**Surajit Bosu**, Baibaswata Bhatterjee,	Springer Singapore, 2022	**ISBN**: 978-981-19-2003-5 https://doi.org/10.1007/978-981-19-2004-2_3

4.	Lecture Notes in Networks and Systems (**LNNS**, Vol. 461), Communication and Intelligent Systems	A Design of Frequency Encoded Dibit-Based Inhibitor Logic Using Reflective Semiconductor Optical Amplifier with Simulative Verification	**Surajit Bosu**, Baibaswata Bhatterjee,	Springer Singapore, 2022	**eBook ISBN**:978-981-19-2130-8 https://doi.org/10.1007/978-981-19-2130-8

Awards:

Best Paper Awards:

1. "All-optical Feynman gate using frequency encoding scheme, Add/Drop multiplexer and reflective semiconductor optical amplifier with simulative verification", **ICCDN-2021, SIKKIM MANIPAL INSTITUTE OF TECHNOLOGY (SMIT), SIKKIM, INDIA**

2. "All-optical frequency encoded Fredkin gate using reflective semiconductor optical amplifier with simulative verification", **ASTMLS 2020, SONA COLLEGE OF TECHNOLOGY, TAMILNADU, INDIA.**